

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Atty. Docket: 00-CCL-096
Yves MORAND et al. : APPLICATIONS BRANCH
Serial No.: (Not Yet Assigned) :
Filed: (Herewith) :
For: *PROCESS FOR FABRICATING A CAPACITOR WITHIN AN INTEGRATED
CIRCUIT, AND CORRESPONDING INTEGRATED CIRCUIT*

J1036 U.S. PTO
09/932513
08/17/01INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D. C. 20231

SIR:

The attached Form PTO-1449 provides a listing of information which may be relevant to the subject application. This IDS is not intended as a representation that better art is not available, nor that the information provided is prior art.

This IDS is submitted under:

- ☒ 37 CFR 1.97(b) - No Fee.
☐ 37 CFR 1.97(c) - No Fee, with Certification.
☐ 37 CFR 1.97(c) - Fee.
☐ 37 CFR 1.97(d) - Fee, Certification & Petition.

The Commissioner is authorized to charge any required fees under 37 CFR 1.17(p) and (i) (1) to Deposit Account No. 50-1556.

Respectfully submitted,

Date: 8/17/01By: Jose Gutman
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37,333

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